

T1/E1 THROUGH HOLE SHIELDED TRANSFORMERS T2741 SERIES



FEATURES:

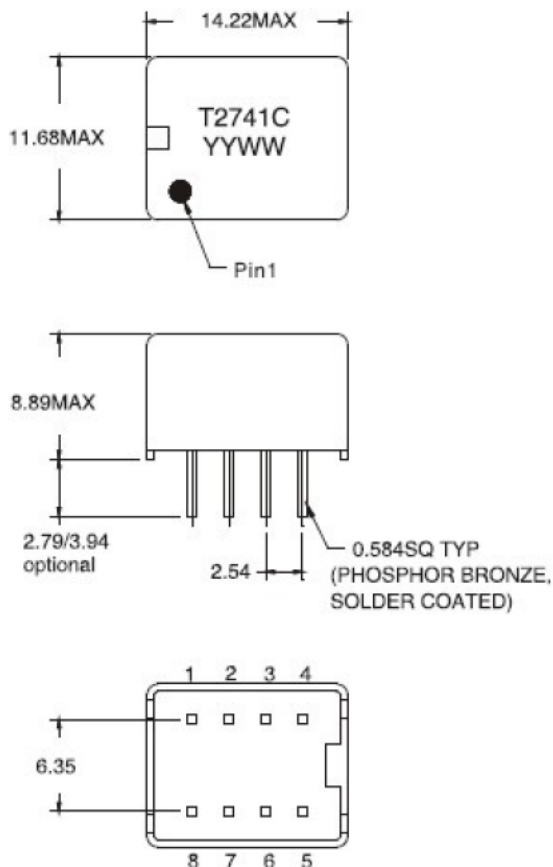
- * Designed for DS1, DS1 C, and CEPT applications
- * Electrostatic shield provides improved longitudinal signal suppression and EMI reduction
- * Maximum inductance unbalance of center-tapped windings is 0.1 %
- * Operating temperature range: -40°C to +85°C
- * Meets IEC 695, 2-2 flammability requirements
- * PWB Process Capability: standard printed wiring board assembly techniques, total-immersion cleaning
- * Reliability testing: Shock, vibration, temperature cycling, temperature - humidity - bias

ELECTRICAL CHARACTERISTICS @25°C:

Part No.	Turns ratio ±2%	Pri. Inductance (mH) Min 10KHz,0.1V	Leakage Inductance (uH) Max	OCR (Q) Max		Interwinding Capacitance (pF) Max	Minimum Interwinding Breakdown Voltage (Vrms)
				Pri	Sec		
T2741B	1CT:2CT	4.5	4.5	3.7	7.2	10	1000
T2741G	1CT:2CT	3.15	6.5	1.9	4.4	5	850
T2741C	1CT:1.36CT	0.92	1.1	0.75	1.05	10	1000
T2741H	1CT:1.37CT	0.92	2.5	0.83	1.43	5	850

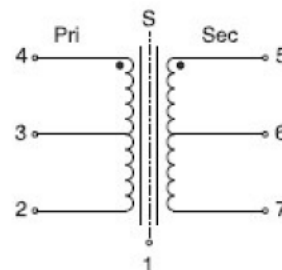
TECHNICAL INFORMATION & PHYSICAL CHARACTERISTICS:

DIMENSIONS: mm

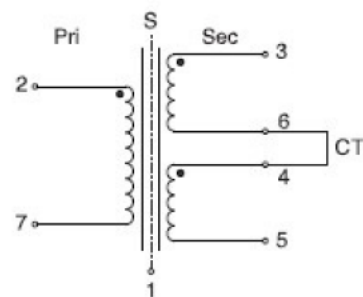


SCHEMATICS

T2741B
T2741G



T2741C
T2741H



- With terminal 1 grounded
- There is an internal connection to terminal 8