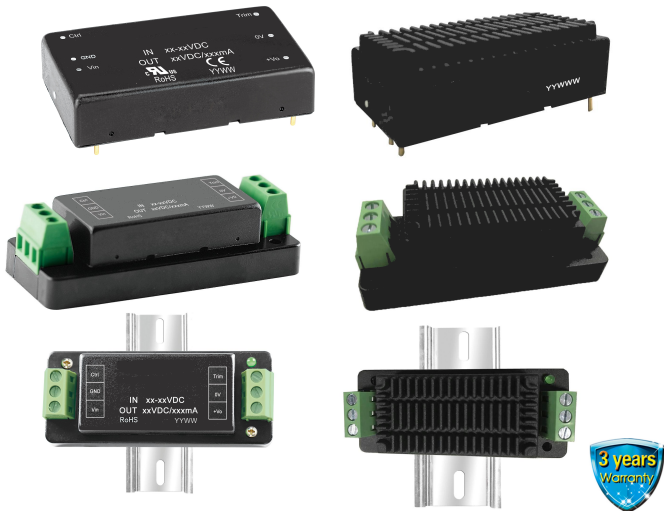


30W isolated DC-DC converter  
Ultra-wide input and regulated dual/single output



**UL** **CE** **CB** Patent Protection **RoHS**

### FEATURES

- Ultra-wide 4:1 input voltage range
- High efficiency up to 90% with full load and up to 82% with 5% load
- No-load power consumption as low as 0.14W
- I/O isolation test voltage: 1.5k VDC
- Input under-voltage protection, output short circuit, over-voltage, over-current protection
- Operating ambient temperature range: -40°C to +80°C
- CISPR32/EN55032 CLASS A EMI compliant without external components
- Six-sided metal shielded package
- Input reverse polarity protection available with chassis(A2S ) or Din-Rail mounting (A4S) version
- IEC60950, UL60950, EN60950 approved
- Meets EN50155 railway standard

*SURA\_LD-30WR3 & SURB\_LD-30WR3 series of isolated 30W DC-DC converter products with an ultra-wide 4:1 input voltage and feature efficiencies of up to 90%, input to output isolation is tested with 1500VDC and the converters safely operate ambient temperature of -40°C to +80°C, input under-voltage protection, output short-circuit, over-voltage, over-current protection. They meet CLASS A of CISPR32/EN55032 EMI standards without external components, optional packages are offered for chassis or DIN-rail mounting (A2S, A4S), adding additional input reverse polarity protection and they are widely used in applications such as data transmission device, battery power supply device, telecommunication device, distributed power supply system, hybrid module system, remote control system, industrial robot and railway fields.*

### Selection Guide

Certification	Part No. ①	Input Voltage (VDC)		Output		Full Load Efficiency <sup>③</sup> (%) Min./Typ.	Max. Capacitive Load(μF) <sup>⑤</sup>
		Nominal <sup>②</sup> (Range)	Max. <sup>③</sup>	Voltage (VDC)	Current (mA) Max./Min.		
UL/CE/CB	SURB2403LD-30WR3	24 (9-36)	40	3.3	6000/0	83/85	10000
	SURB2405LD-30WR3			5	6000/0	84/86	10000
	SURB2409LD-30WR3			9	3333/0	86/88	4700
	SURB2412LD-30WR3			12	2500/0	88/90	2700
	SURB2415LD-30WR3			15	2000/0	88/90	1680
	SURB2424LD-30WR3			24	1250/0	88/90	680
CE	SURA2405LD-30WR3			±5	±3000/0	84/86	2000
	SURA2412LD-30WR3			±12	±1250/0	87/89	1250
	SURA2415LD-30WR3			±15	±1000/0	87/89	680
	SURA2424LD-30WR3			±24	±625/0	87/89	470
UL/CE/CB	SURB4803LD-30WR3	48 (18-75)	80	3.3	6000/0	84/86	10000
	SURB4805LD-30WR3			5	6000/0	85/87	10000
	SURB4812LD-30WR3			12	2500/0	86/88	2700
	SURB4815LD-30WR3			15	2000/0	87/89	1680
	SURB4824LD-30WR3			24	1250/0	85/87	680
CE	SURA4805LD-30WR3			±5	±3000/0	84/86	2000
	SURA4812LD-30WR3			±12	±1250/0	86/88	1250
	SURA4815LD-30WR3			±15	±1000/0	86/88	680

Notes: ①Use "H" suffix for heat sink mounting, "A2S" suffix for chassis mounting and "A4S" suffix for DIN-Rail mounting. We recommend to choose modules with a heat sink for enhanced heat dissipation and applications with extreme temperature requirements;  
 ②Minimum input voltage and start-up voltage are increased by 1V for all models with A2S and A4S suffixes because of the input reverse polarity function;  
 ③Exceeding the maximum input voltage may cause permanent damage;  
 ④Efficiency is measured at nominal input voltage and rated output load; efficiencies for A2S and A4S Model's is decreased by 2% due to the input reverse polarity protection circuit;  
 ⑤The specified maximum capacitive load value for positive and negative output is identical.

# DC/DC Converter

## SURA\_LD-30WR3 & SURB\_LD-30WR3 Series

### Input Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit	
Input Current (full load / no-load)	24VDC nominal input series, nominal input voltage	3.3V output	--	970/60	993/100	mA
		5V output	--	1454/60	1488/100	
		Others	--	1388/6	1488/16	
	48VDC nominal input series, nominal input voltage	3.3V output	--	474/20	485/30	
		5V output	--	710/20	726/35	
		Others	--	702/5	744/10	
Reflected Ripple Current	Nominal input voltage	--	40	--		
Surge Voltage (1sec. max.)	24VDC nominal input series	-0.7	--	50	VDC	
	48VDC nominal input series	-0.7	--	100		
Start-up Voltage	24VDC nominal input series	--	--	9		
	48VDC nominal input series	--	--	18		
Shut-down Voltage	24VDC nominal input series	5.5	6.5	--		
	48VDC nominal input series	12.0	15.5	--		
Start-up Time	Nominal input voltage & constant resistance load	--	10	--	ms	
Input Filter		PI filter				
Hot Plug		Unavailable				
Ctrl *	Module on	Ctrl pin open or pulled high (3.5-12VDC)				
	Module off	Ctrl pin pulled low to GND (0-1.2VDC)				
	Input current when off	--	5	8	mA	

Note: \*The Ctrl pin voltage is referenced to input GND.

### Output Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit	
Voltage Accuracy	5%-100% load	--	±1	±3	%	
	0%-5% load	--	±1	±5		
Linear Regulation	Input voltage variation from low to high at full load	Vo1	--	±0.2		±0.5
		Vo2	--	±0.5		±1
Load Regulation <sup>①</sup>	5%-100% load	Vo1	--	±0.5		±1
		Vo2	--	±0.5		±1.5
Cross Regulation	Dual output, Vo1 load at 50%, Vo2 load at range of 10%-100%	--	--	±5		
Transient Recovery Time		--	300	500	μs	
Transient Response Deviation	25% load step change, nominal input voltage	3.3V/5V/±5V output	--	±5	±8	%
		Others	--	±3	±5	
Temperature Coefficient	Full load	--	--	±0.03	%/°C	
Ripple & Noise <sup>②</sup>	20MHz bandwidth, nominal input voltage, 100% load	Singe output	--	50	100	Mv p-p
		Dual output	--	50	150	
Trim		--	±10	--	%Vo	
Over-voltage Protection	Input voltage range	110	--	160		
Over-current Protection		110	--	190		
Short-circuit Protection		Hiccup, continuous, self-recovery				

Note: ①Load regulation for 0%-100% load is ±5%;  
②The "parallel cable" method is used for ripple and noise test, please refer to *DC-DC Converter Application Notes* for specific information.

# DC/DC Converter

## SURA\_LD-30WR3 & SURB\_LD-30WR3 Series

### General Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit
Isolation	Input-output Electric Strength test for 1 minute with a leakage current of 1mA max.	1500	--	--	VDC
Insulation Resistance	Input-output resistance at 500VDC/60sec	1000	--	--	MΩ
Isolation Capacitance	Input-output capacitance at 100KHz/0.1V	--	2000	--	pF
Operating Temperature	See Fig. 1, Fig. 2, Fig. 3 and Fig. 4	-40	--	+80	°C
Storage Temperature		-55	--	+125	
Storage Humidity	Non-condensing	5	--	95	%RH
Pin Soldering Resistance Temperature	Soldering spot is 1.5mm away from case for 10 seconds	--	--	+300	°C
Vibration		IEC/EN61373 - Category 1, Grade B			
Switching Frequency *	PWM mode	--	300	--	KHz
MTBF	MIL-HDBK-217F@25°C	1000	--	--	K hours

Note:\* Switching frequency is measured at full load. The module reduces the switching frequency for light load (below 50%) efficiency improvement.

### Mechanical Specifications

Case Material	Aluminum alloy				
Dimensions	Horizontal package (without heat sink)			50.80 x 25.40 x 11.80 mm	
	Horizontal package (with heat sink)			51.40 x 26.20 x 16.50 mm	
	A2S chassis mounting (without heat sink)			76.00 x 31.50 x 21.20 mm	
	A2S chassis mounting (with heat sink)			76.00 x 31.50 x 25.30 mm	
	A4S Din-rail mounting (without heat sink)			76.00 x 31.50 x 25.80 mm	
	A4S Din-rail mounting (with heat sink)			76.00 x 31.50 x 29.90 mm	
Weight	without heat sink	Horizontal package/A2S chassis mounting/A4S Din-rail mounting		27.8g/52.0g/72.0g(Typ.)	
	with heat sink	Horizontal package/A2S chassis mounting/A4S Din-rail mounting		37.0g/60.0g/80.0g(Typ.)	
Cooling Method	Free air convection				

### Electromagnetic Compatibility (EMC)

Emissions	CE	Single output	CISPR32/EN55032	CLASS A (without external components)/ CLASS B (see Fig.6-② for recommended circuit)	
		Dual output	CISPR32/EN55032	CLASS A (without external components)/ CLASS B (see Fig.7-② for recommended circuit)	
	RE	Single output	CISPR32/EN55032	CLASS A (without external components)/ CLASS B (see Fig.6-② for recommended circuit)	
		Dual output	CISPR32/EN55032	CLASS A (without external components)/ CLASS B (see Fig.7-② for recommended circuit)	
Immunity	ESD		IEC/EN61000-4-2	Contact ±4KV perf. Criteria B	
	RS		IEC/EN61000-4-3	10V/m perf. Criteria A	
	EFT	Single output	IEC/EN61000-4-4	±2KV (see Fig.6-① for recommended circuit) perf. Criteria B	
		Dual output	IEC/EN61000-4-4	±2KV (see Fig.7-① for recommended circuit) perf. Criteria B	
	Surge	Single output	IEC/EN61000-4-5	line to line ±2KV (see Fig.6-① for recommended circuit) perf. Criteria B	
		Dual output	IEC/EN61000-4-5	line to line ±2KV (see Fig.7-① for recommended circuit) perf. Criteria B	
	CS	Single output	IEC/EN61000-4-6	3 Vr.m.s perf. Criteria A	
		Dual output	IEC/EN61000-4-6	10Vr.m.s perf. Criteria A	

### Electromagnetic Compatibility (EMC) (EN50155)

Emissions	CE	Single output	EN50121-3-2	150kHz-500kHz	99dBuV (see Fig.6-② for recommended circuit)
			EN55016-2-1	500kHz-30MHz	93dBuV (see Fig.6-② for recommended circuit)
		Dual output	EN50121-3-2	150kHz-500kHz	99dBuV (see Fig.7-② for recommended circuit)
	RE	Single output	EN55016-2-1	500kHz-30MHz	93dBuV (see Fig.7-② for recommended circuit)
			EN50121-3-2	30MHz-230MHz	40dBuV/m at 10m (see Fig.6-② for recommended circuit)
		Dual output	EN55016-2-1	230MHz-1GHz	47dBuV/m at 10m (see Fig.6-② for recommended circuit)
		EN50121-3-2	30MHz-230MHz	40dBuV/m at 10m (see Fig.7-② for recommended circuit)	

# DC/DC Converter

## SURA\_LD-30WR3 & SURB\_LD-30WR3 Series

Emissions	RE	Dual output	EN55016-2-1	230MHz-1GHz	47dBuV/m at 10m	(see Fig.7-② for recommended circuit)	
Immunity	ESD		EN50121-3-2	Contact ±6KV/Air ±8KV		perf. Criteria A	
	RS		EN50121-3-2	20V/m		perf. Criteria A	
	EFT	Single output	EN50121-3-2	±2kV	5/50ns	5kHz	(see Fig.6-① for recommended circuit) perf. Criteria A
		Dual output	EN50121-3-2	±2kV	5/50ns	5kHz	(see Fig.7-① for recommended circuit) perf. Criteria A
	Surge	Single output	EN50121-3-2	line to line ±1KV (42Ω, 0.5μF)			(see Fig.6-① for recommended circuit) perf. Criteria A
		Dual output	EN50121-3-2	line to line ±1KV (42Ω, 0.5μF)			(see Fig.7-① for recommended circuit) perf. Criteria A
	CS	Single output	EN50121-3-2	0.15MHz-80MHz	10V r.m.s		perf. Criteria A
Dual output		EN50121-3-2	0.15MHz-80MHz	10V r.m.s		perf. Criteria A	

### Typical Characteristic Curves

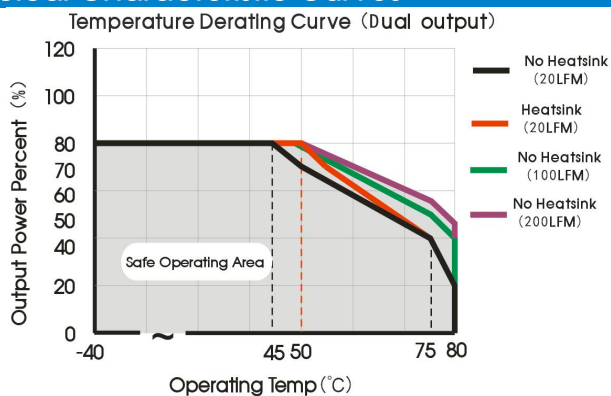


Fig. 1

Apply model: SURA2405LD-30W(H)R3 (9-18V input voltage),  
SURA2424LD-30W(H)R3 (9-18V input voltage),  
SURA4805LD-30W(H)R3 (18-36V input voltage)

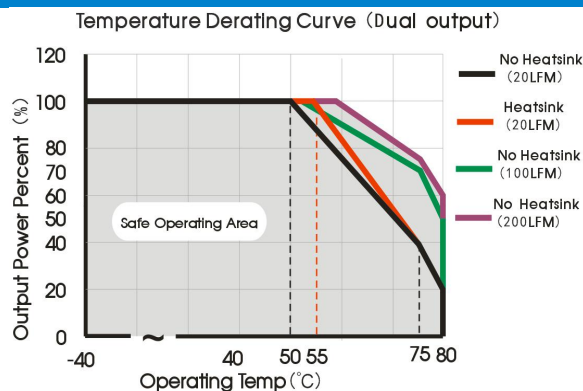


Fig. 2

Apply model: SURA2405LD-30W(H)R3 (18-36V input voltage),  
SURA2424LD-30W(H)R3 (18-36V input voltage),  
SURA4805LD-30W(H)R3 (36-75V input voltage),  
SURA2412LD-30W(H)R3, SURA2415LD-30W(H)R3,  
SURA4812LD-30W(H)R3, SURA4815LD-30W(H)R3

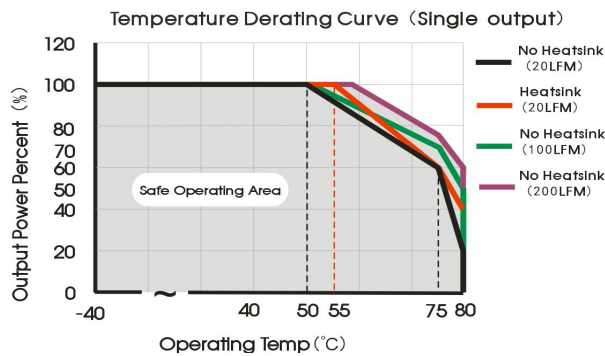


Fig. 3

Apply model: SURB2403LD-30W(H)R3, SURB2405LD-30W(H)R3,  
SURB4803LD-30W(H)R3, SURB4805LD-30W(H)R3

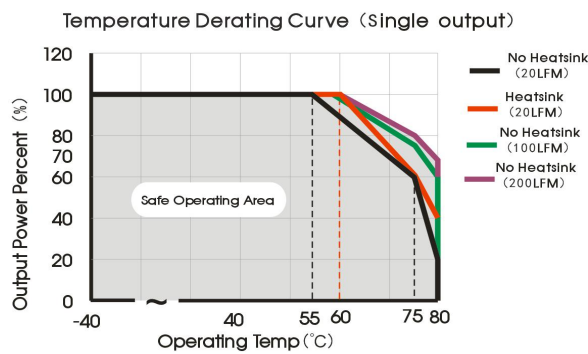


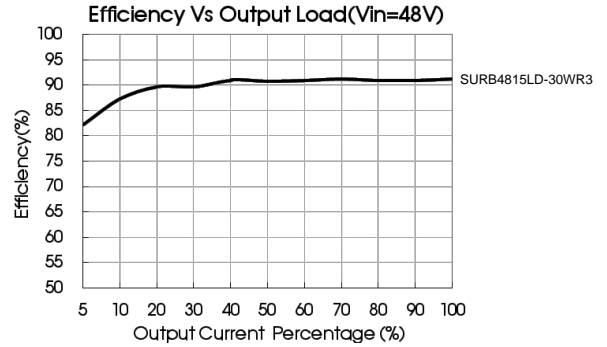
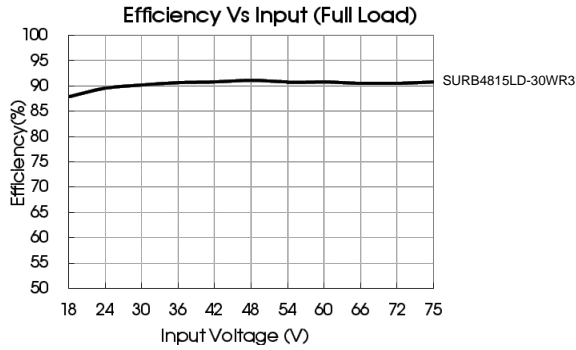
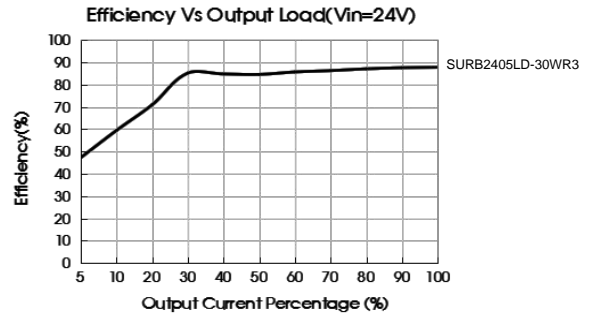
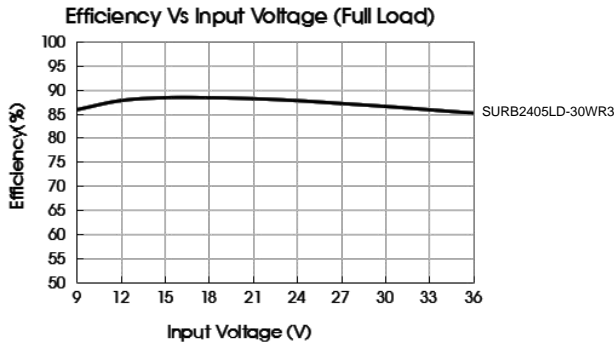
Fig. 4

Apply model: SURB2409LD-30W(H)R3, SURB2412LD-30W(H)R3,  
SURB2415LD-30W(H)R3, SURB2424LD-30W(H)R3,  
SURB4812LD-30W(H)R3, SURB4815LD-30W(H)R3,  
SURB4824LD-30W(H)R3



# DC/DC Converter

## SURA\_LD-30WR3 & SURB\_LD-30WR3 Series



## Design Reference

### 1. Typical application

All the DC/DC converters of this series are tested before delivery using the recommended circuit shown in Fig. 2.

Input and/or output ripple can be further reduced by appropriately increasing the input & output capacitor values  $C_{in}$  and  $C_{out}$  and/or by selecting capacitors with a low ESR (equivalent series resistance). Also make sure that the capacitance is not exceeding the specified max. capacitive load value of the product.

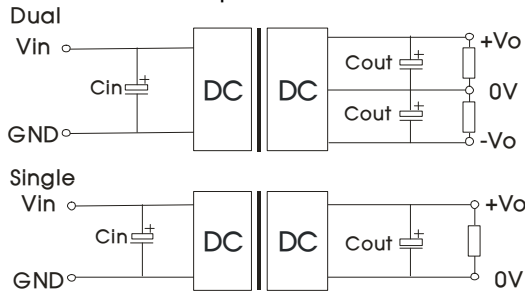


Fig. 5

Single output voltage (VDC)	$C_{out}$ ( $\mu F$ )	$C_{in}$ ( $\mu F$ )	Dual output voltage (VDC)	$C_{out}$ ( $\mu F$ )	$C_{in}$ ( $\mu F$ )
3.3/5/9	220	100	$\pm 5/\pm 12/\pm 15$	220	100
12/15/24	100		$\pm 24$	100	

### 2. EMC compliance circuit

Single output

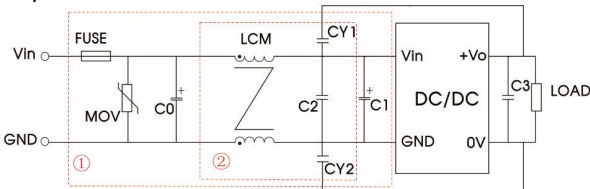


Fig. 6

Notes: For EMC tests we use Part ① in Fig. 6 for immunity and part ② for emissions test.

Parameter description

Model	Vin:24V	Vin:48V
FUSE	Choose according to actual input current	
MOV	S20K30	S14K60
C0	680 $\mu F$ /50V	330 $\mu F$ /100V
C1	330 $\mu F$ /50V	330 $\mu F$ /100V
C2	4.7 $\mu F$ /50V	2.2 $\mu F$ /100V
C3	Refer to the $C_{out}$ in Fig.5	
LCM	1mH, recommended to use SCHMID-M's SFL2D-30-102	
CY1, CY2	1nF/2KV	

# DC/DC Converter

## SURA\_LD-30WR3 & SURB\_LD-30WR3 Series

Dual output

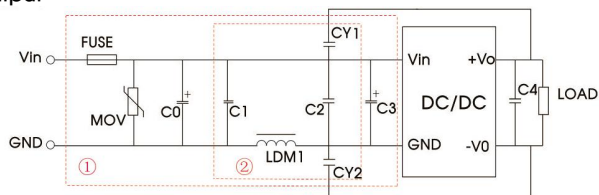
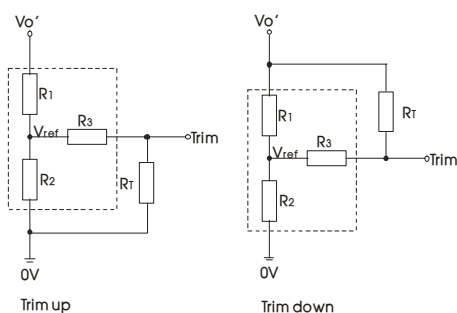


Fig.7

Notes: For EMC tests we use Part ① in Fig. 7 for immunity and part ② for emissions test.

Model	Vin:24V	Vin:48V
FUSE	Choose according to actual input current	
MOV	S20K30	S14K60
C0	680μF/50V	330μF/100V
C1, C2	2.2μF/50V	2.2μF/100V
C3	330μF/50V	330μF/100V
C4	Refer to the Cout in Fig.5	
LDM1	3.3μH	
CY1, CY2	2.2nF/400VAC Safety Y Capacitor	

### 3. Trim Function for Output Voltage Adjustment (open if unused)



Calculating Trim resistor values:

$$\text{up: } R_T = \frac{\alpha R_2}{R_2 - \alpha} - R_3 \quad \alpha = \frac{V_{ref}}{V_{o'} - V_{ref}} \cdot R_1$$

$$\text{down: } R_T = \frac{\alpha R_1}{R_1 - \alpha} - R_3 \quad \alpha = \frac{V_{o'} - V_{ref}}{V_{ref}} \cdot R_2$$

$R_T$  = Trim Resistor value;  
 $\alpha$  = self-defined parameter;  
 $V_{o'}$  = desired output voltage.

TRIM resistor connection (dashed line shows internal resistor network)

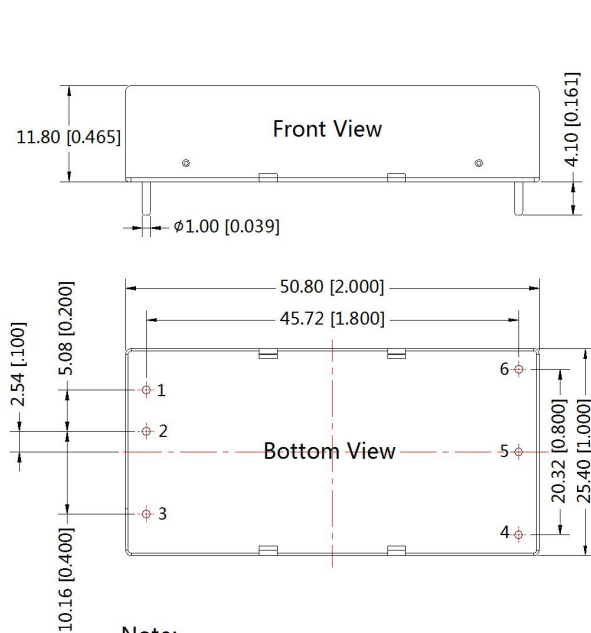
Vout(VDC)	R1(KΩ)	R2(KΩ)	R3(KΩ)	Vref(V)
3.3	4.801	2.87	12.4	1.24
5	2.883	2.87	10	2.5
9	7.500	2.87	15	2.5
12	11.000	2.87	15	2.5
15	14.494	2.87	15	2.5
24	24.872	2.87	17.8	2.5

### 4. The products do not support parallel connection of their output

# DC/DC Converter

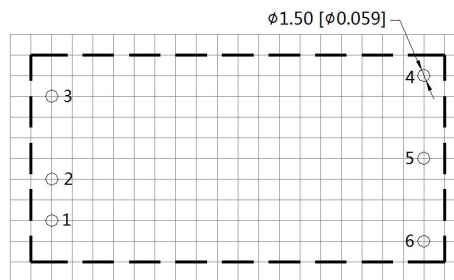
## SURA\_LD-30WR3 & SURB\_LD-30WR3 Series

### Horizontal Package (without heat sink) Dimensions and Recommended Layout



Note:  
Unit :mm[inch]  
Pin diameter tolerances : $\pm 0.10[\pm 0.004]$   
General tolerances: $\pm 0.50[\pm 0.020]$

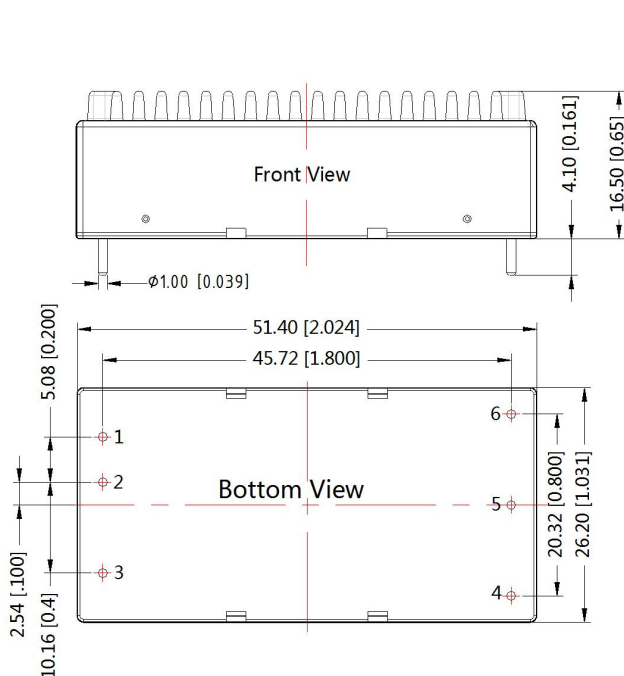
THIRD ANGLE PROJECTION



Note : Grid 2.54\*2.54mm

Pin-Out		
Pin	Single	Dual
1	Vin	Vin
2	GND	GND
3	Ctrl	Ctrl
4	Trim	-Vo
5	0V	0V
6	+Vo	+Vo

### Horizontal Package (with heat sink) Dimensions



THIRD ANGLE PROJECTION


Pin-Out		
Pin	Single	Dual
1	Vin	Vin
2	GND	GND
3	Ctrl	Ctrl
4	Trim	-Vo
5	0V	0V
6	+Vo	+Vo

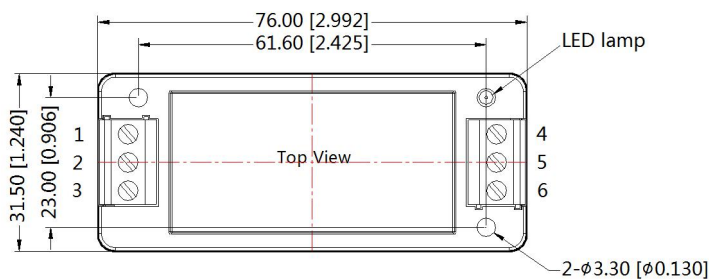
Note:  
Unit: mm[inch]  
General tolerances:  $\pm 0.50[\pm 0.020]$

# DC/DC Converter

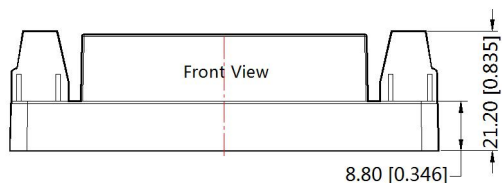
## SURA\_LD-30WR3 & SURB\_LD-30WR3 Series

### SURA\_LD-30WR3A2S & SURB\_LD-30WR3A2S(without heat sink) Dimensions

THIRD ANGLE PROJECTION 



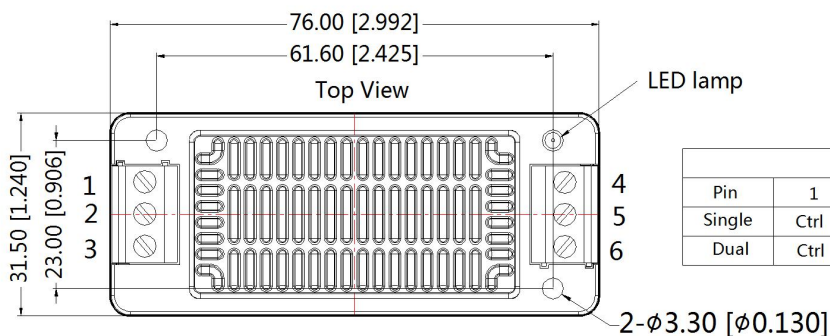
Pin-Out						
Pin	1	2	3	4	5	6
Single	Ctrl	GND	Vin	Trim	0V	+Vo
Dual	Ctrl	GND	Vin	-Vo	0V	+Vo



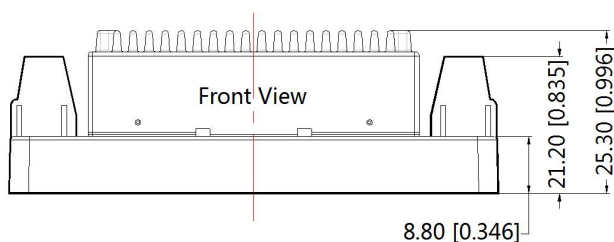
Note:  
 Unit: mm[inch]  
 Wire range: 24-12 AWG  
 Tightening torque: Max 0.4 N·m  
 General tolerances: ±0.50[±0.020]

### SURA\_LD-30WR3A2S & SURB\_LD-30WR3A2S(with heat sink) Dimensions

THIRD ANGLE PROJECTION 



Pin-Out						
Pin	1	2	3	4	5	6
Single	Ctrl	GND	Vin	Trim	0V	+Vo
Dual	Ctrl	GND	Vin	-Vo	0V	+Vo



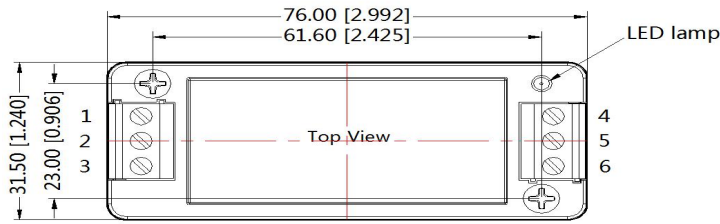
Note:  
 Unit: mm[inch]  
 Wire range: 24-12 AWG  
 Tightening torque: Max 0.4 N·m  
 General tolerances: ±0.50[±0.020]

# DC/DC Converter

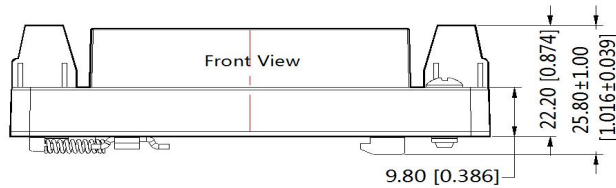
## SURA\_LD-30WR3 & SURB\_LD-30WR3 Series

### SURA\_LD-30WR3A4S & SURB\_LD-30WR3A4S(without heat sink) Dimensions

THIRD ANGLE PROJECTION 



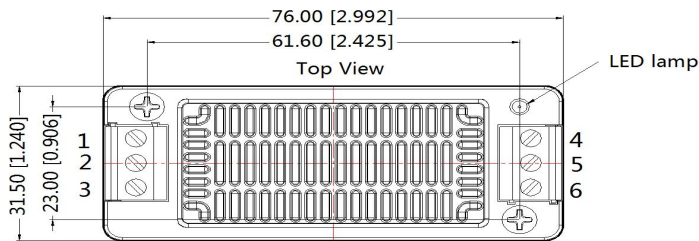
		Pin-Out					
Pin		1	2	3	4	5	6
Single		Ctrl	GND	Vin	Trim	0V	+Vo
Dual		Ctrl	GND	Vin	-Vo	0V	+Vo



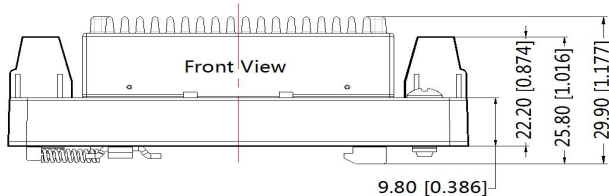
Note:  
 Unit: mm[inch]  
 Wire range: 24-12 AWG  
 Tightening torque: Max 0.4 N·m  
 General tolerances: ±0.50[±0.020]

### SURA\_LD-30WR3A4S & SURB\_LD-30WR3A4S(with heat sink) Dimensions

THIRD ANGLE PROJECTION 



		Pin-Out					
Pin		1	2	3	4	5	6
Single		Ctrl	GND	Vin	Trim	0V	+Vo
Dual		Ctrl	GND	Vin	-Vo	0V	+Vo



Note:  
 Unit: mm[inch]  
 Mounting rail: TS35  
 Wire range: 24-12 AWG  
 Tightening torque: Max 0.4 N·m  
 General tolerances: ±1.00[±0.039]

#### Notes:

1. The maximum capacitive load offered were tested at input voltage range and full load;
2. Unless otherwise specified, parameters in this datasheet were measured under the conditions of  $T_a=25^\circ\text{C}$ , humidity<75%RH with nominal input voltage and rated output load;
3. All index testing methods in this datasheet are based on company corporate standards;
4. We can provide product customization service, please contact our technicians directly for specific information;
5. Products are related to laws and regulations: see "Features" and "EMC";
6. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.