# DC/DC Converter SA XT-1WR3 Series



1W isolated DC-DC converter
Fixed input voltage, unregulated dual output



Patent Protection RoHS





# **FEATURES**

- Continuous short-circuit protection
- No-load input current as low as 8mA
- High efficiency up to 85%
- Compact SMD package
- I/O isolation test voltage: 1.5k VDC
- Industry standard pin-out

SA\_XT-1WR3 series are specially designed for applications where two isolated voltage is required in a distributed power supply system. They are suitable for: pure digital circuits, low frequency analog circuits, relay-driven circuits and data switching circuits.

	Part No.	Input Voltage (VDC)	0	utput	Full Load	Capacitive
Certification		Nominal (Range)	Voltage (VDC)	Current (mA) Max./Min.	Efficiency (%) Min./Typ.	Load(µF) Max.*
	SA1205XT-1WR3		±5	±100/±10	78/82	1200
	SA1209XT-1WR3		±9	±56/±6	79/83	470
	SA1212XT-1WR3	12 (10.8-13.2)	±12	±42/±5	79/83	220
	SA1215XT-1WR3	(10.0 10.2)	±15	±34/±4	79/83	220
	SA1224XT-1WR3		±24	±21/±3	81/85	100
	SA 1515XT-1WR3	15 (13.5-16.5)	±15	±34/±4	79/83	220
	SA2405XT-1WR3		±5	±100/±10	76/82	1200
	SA2409XT-1WR3		±9	±56/±6	77/83	470
	SA2412XT-1WR3	24 (21.6-26.4)	±12	±42/±5	77/83	220
	SA2415XT-1WR3	(2110 2014)	±15	±34/±4	77/83	220
	SA2424XT-1WR3		±24	±21/±3	79/85	100

Note: \*The specified maximum capacitive load for positive and negative output is identical.

ltem	Operating 0	Conditions	Min.	Тур.	Max.	Unit
		±5VDC output		102/8	107/	
	12V input	±9VDC/±12VDC/±15VDC output	-	101/8	106/	
Input Current	±24VDC output		-	99/8	103/	mA
(full load / no-load)	15V input			81/8	85/	
	±5VDC/±9VDC/±12VDC/±15V 24V input output			51/8	55/	
		±24VDC output		50/8	53/	
Reflected Ripple Current*				30		
	12VDC input 15VDC input 24VDC input		-0.7		18	VDC
Surge Voltage(1sec. max.)			-0.7		21	
			-0.7		30	
nput Filter				Capacit	ance filter	
Hot Plug				Unav	ailable	

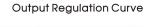
Item	Operating Condition	s	Min.	Typ.	Max.	Unit
Voltage Accuracy	operaning consumer		output regula			
Linear Regulation	Input voltage change	Input voltage change: ±1%			1.2	
	10%-100% load	±5VDC output		10	15	%
		±9VDC output		8	10	
Load Regulation		±12VDC output		7	10	
		±15VDC output		6	10	
		±24VDC output		5	10	
Ripple & Noise*	20MHz bandwidth	±5VDC/±9VDC/±12VDC/± 15VDC output		30	75	mVp-p
	±24VDC output			50	100	
Temperature Coefficient	Full load		-	±0.02		%/℃
Short-Circuit Protection				Continuous,	self-recovery	

General Specification	S					
Item	Operating Conditions	Min.	Тур.	Max.	Unit	
Isolation	Input-output electric strength test for 1 minute with a leakage current of 1mA max.	1500		VD0		
Insulation Resistance	Input-output resistance at 500VDC	1000	_		$\mathbf{M}\Omega$	
Isolation Capacitance	Input-output capacitance at 100kHz/0.1V		20		рF	
Operating Temperature	Derating when operating temperature $\geqslant$ 100 $^{\circ}$ C, (see Fig. 2)	-40		105		
Storage Temperature		-55	_	125	$^{\circ}$	
Case Temperature Rise	Ta=25℃		25			
Storage Humidity	Non-condensing	5	-	95	%RH	
Reflow Soldering Temperature*	Reflow Soldering Temperature*  Peak temp.≤245°C, maximum duration ti over 217°C					
Switching Frequency	Full load, nominal input voltage		260		kHz	
MTBF	MIL-HDBK-217F@25°C	3500			k hours	
Moisture Sensitivity Level (MSL)	IPC/JEDEC J-STD-020D.1	Level 1				
Note: * For actual application, please	e refer to IPC/JEDEC J-STD-020D.1.					

Mechanical Specifica	Mechanical Specifications					
Case Material	Black plastic; flame-retardant and heat-resistant (UL94 V-0)					
Dimensions	5.24 x 11.40 x 7.25 mm					
Weight	1.4g(Typ.)					
Cooling Method	Free air convection					

Electromagnetic Compatibility (EMC)								
Emissions	CE	CISPR32/EN55032 CLASS B (see Fig. 4 for recommended circuit)						
	RE	CISPR32/EN55032 CLASS B (see Fig. 4 for recommended circuit)						
Immunity	ESD	IEC/EN61000-4-2 Contact ±6kV perf. Criteria B						

## Typical Performance Curves



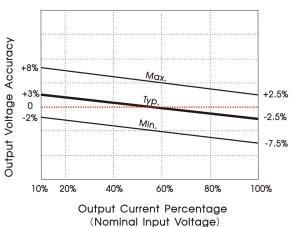


Fig. 1

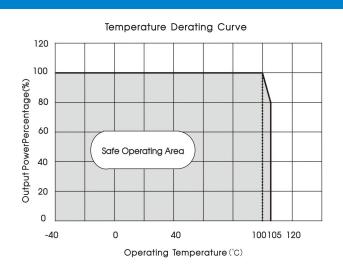
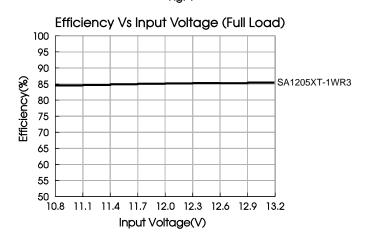
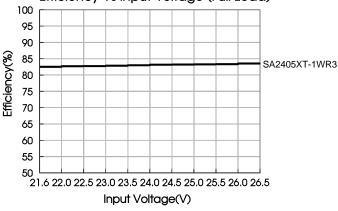
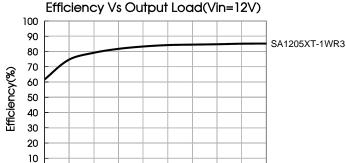


Fig. 2

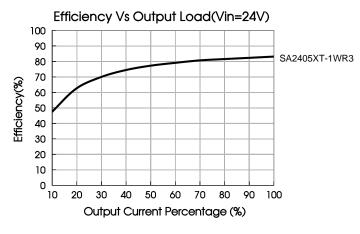


Efficiency Vs Input Voltage (Full Load)





30 40 50 60 70 80 9 Output Current Percentage (%)



## Design Reference

#### 1. Typical application

Input and/or output ripple can be further reduced, by connecting a filter capacitor from the input and/or output terminals to ground as shown in Fig.3.

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Choosing suitable filter capacitor values is very important for a smooth operation of the modules, particularly to avoid start-up problems caused by capacitor values that are too high. For recommended input and output capacitor values refer to Table 1.

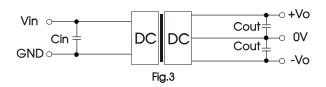


Table 1: Recommended input and output capacitor values

Vin	Cin	Vo	Cout
12VDC	2.2µF/25V	±5VDC	4.7µF/16V
15VDC	2.2µF/25V	±9VDC	1µF/16V
24VDC	1µF/50V	±12VDC	1µF/25V
		±15VDC	0.47µF/25V
		±24VDC	0.47µF/50V

2. EMC (CLASS B) compliance circuit

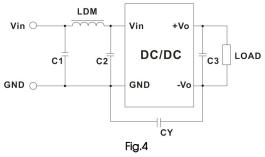


Table 2: EMC recommended circuit value table

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	C1	4.7µF/50V					
Emissions	C2	4.7µF/50V					
	CY	270pF/2kV					
	СЗ	Refer to the Cout in table 1					
	LDM	6.8µH					

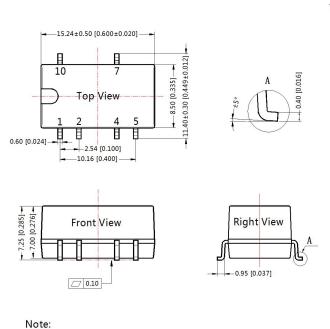
#### 3. Minimum Output Load Requirement

For a reliable and efficient operation of the converter, the minimum load should never be less than 10% of the rated output load. If the total required output power is below 10%, a parallel bleeding resistor is required on the output, ensuring that the sum of the power consumption is always maintained at 10% minimum.

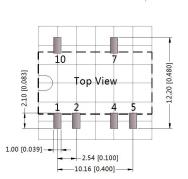
# **Dimensions and Recommended Layout**

Unit: mm[inch]

Pin section tolerances:  $\pm 0.10[\pm 0.004]$ General tolerances:  $\pm 0.25[\pm 0.010]$ 



THIRD ANGLE PROJECTION 💮 🧲

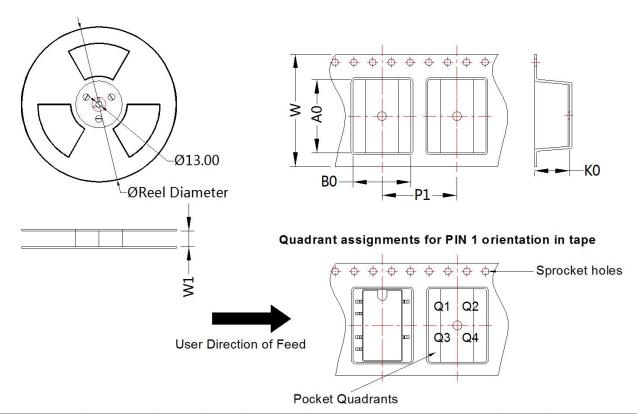


Note: Grid 2.54\*2.54mm

Pin-Out							
Pin	Function						
1	GND						
2	Vin						
4	0V						
5	-Vo						
7	+Vo						
10	NC						

NC: Pin to be isolated from circuitry

# Tape and Reel Info



	Device	Package Type	Pin	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SA	_XT-1WR3	SMD	6	500	330.0	24.5	15.64	12.4	7.45	16.0	24.0	Q1

#### Notes:

- 1. If the product is not operated within the required load range, the product performance cannot be guaranteed to comply with all parameters in the datasheet;
- 2. The maximum capacitive load offered were tested at input voltage range and full load;
- 3. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 4. All index testing methods in this datasheet are based on our company corporate standards;
- 5. We can provide product customization service, please contact our technicians directly for specific information;
- 6. Products are related to laws and regulations: see "Features" and "EMC";
- 7. Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.