DC/DC Converter

SIB05_S-W75R3 Series



0.75W, Fixed input voltage, isolated & regulated single output





FEATURES

- Continuous short-circuit protection
- No-load input current as low as 5mA
- Operating temperature range: -40°C to +85°C
- High efficiency up to 74%
- Isolation voltage: 3K VDC
- International standard pin-out
- Compact SIP package
- Meets UL62368, EN62368 standards(Pending)

SIB05_S-W75R3 series are specially designed for applications where an isolated voltage is required in a distributed power supply system. They are suitable for; preceding-stage interference isolation condition; ground-interference canceled condition; digit circuit condition; Voltage-isolation converting condition; normal low-frequency artificial circuit condition; relay drive circuit condition, etc.

Selection G	Selection Guide						
		Input Voltage (VDC)	Output		Efficiency		
Certification Part No.		Nominal (Range)	Output Voltage (VDC)	Output Current (mA) (Max./Min.)	(%,Min./Typ.) @ Full Load	Max. Capacitive Load* (µF)	
	SIB0503S-W75R3		3.3	200/20	64/68	2400	
	SIB0505S-W75R3	5 (4.75-5.25)	5	150/15	68/72	2400	
UL/CE (Pending)	SIB0509S-W75R3		9	83/9	68/72	1000	
(i orialily)	SIB0512S-W75R3		12	62/7	69/73	560	
	SIB0515S-W75R3		15	50/5	70/74	560	

Input Specifications					
Item	Operating Conditions	Min.	Тур.	Max.	Unit
	3.3VDC/5VDC output		209/5	221/10	mA
Input Current (full load / no-load)	9VDC/12VDC output	-	208/12	221/20	
	15VDC output	-	202/18	215/30	
Reflected Ripple Current			15		
Input Filter Filter Capacitor					
Hot Plug Unavailable					
Note: * Reflected ripple current to	esting method please see DC-DC Converter Application Notes f	or specific operc	ition.		

Output Specification	าร					
Item	Operating Conditions	Min.	Тур.	Max.	Unit	
Output Voltage Accuracy					±3	%
Line Regulation	Input voltage change: ±1%			-	±0.25	%
Load Regulation	10%-100% load	3.3VDC output		-	±3	%
		Other outputs		-	±2	
Ripple & Noise*	20MHz bandwidth		30	75	mVp-p	
Temperature Coefficient	100% load		±0.02	_	%/℃	
Short Circuit Protection				Continuous,	self-recovery	,
Note: * Ripple and noise are meas	sured by "parallel cable" met	thad please see DC-DC Convert	er Application Note	es for specific o	peration	

General Specifications					
Item	Operating Conditions	Min.	Тур.	Max.	Unit
la colada a Vallaca	Input-output, with the test time of 1 minute and the leak current lower than 1mA	1500			VDC
Insulation Voltage	Input-output, with the test time of 1 second and the leak current lower than 1mA	3000			VDC

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DC/DC Converter

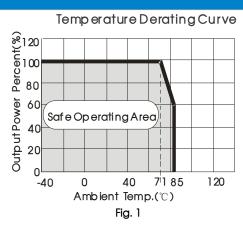
SIB05_S-W75R3 Series

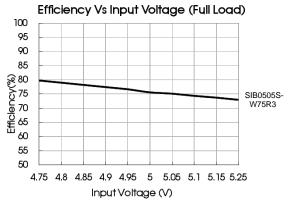
Insulation Resistance	Input-output, isolation	Input-output, isolation voltage 500VDC				ΜΩ
Isolation Capacitance	Input-output, 100KHz/	0.1V		20	-	pF
Operating Temperature	Derating when opera	tting temperature up to 71°C(see Fig. 2)	-40		85	
Storage Temperature			-55		125	
Casing Temperature Rise	T. 05°0	3.3VDC output		30	_	T C
	Ta=25°C	Other outputs		25		
Pin Welding Resistance Temperature	Welding spot is 1.5mm	Welding spot is 1.5mm away from the casing, 10 seconds			300	
Storage Humidity	Non-condensing	Non-condensing			95	%RH
Vibration				z, 2G, 30 M	in. along X	, Y and Z
Switching Frequency	100% load, nominal in	100% load, nominal input voltage			_	KHz
MTBF	MIL-HDBK-217F@25℃		3500			K hours

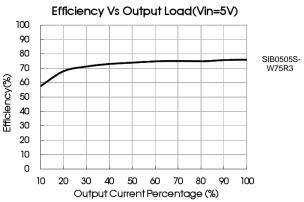
Physical Specifications	
Casing Material	Black flame-retardant and heat-resistant plastic (UL94 V-0)
Dimensions	11.60*6.00*10.16mm
Weight	1.3g(Typ.)
Cooling Method	Free air convection

EMC Specifications					
ЕМІ	CE	CISPR32/EN55032 CLASS B (see Fig. 3 for recommended circuit)			
	RE	CISPR32/EN55032 CLASS B (see Fig. 3 for recommended circuit)			
EMS	ESD	IEC/EN61000-4-2 Air ±8kV , Contact ±4kV perf. Criteria B			

Product Characteristic Curve



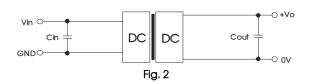




Design Reference

1. Typical application circuit

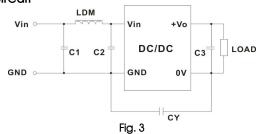
If it is required to further reduce input and output ripple, a filter capacitor may be connected to the input and output terminals, see Fig.3. Moreover, choosing a suitable filter capacitor is very important, start-up problems may be caused if the capacitance is too large. Under the condition of safe and reliable operation, the recommended capacitive load values are shown in Table 1.



Recommended capacitive load value table (Table 1)

Vin(VDC)	Cin(µF)	Vo (VDC)	Cout(µF)
5	4.7	3.3/5	10
		9/12	2.2
-	-	15	1

2. EMC solution-recommended circuit

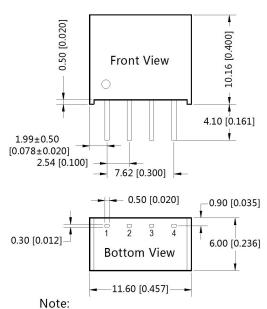


EMC recommended circuit value table (Table 2)

LIVIC recommended circuit value table (table 2)					
	Output v	oltage (VDC)	3.3/5/9	12/15	
		C1/C2	4.7µF /25V	4.7µF /25V	
Input voltage 5VDC	voltage	СУ		1nF/4KVDC VISHAY HGZ102MBP TDK CD45-E2GA102M-GKA	
		C3	Refer to the Cout in table 1		
		LDM	6.8µH	6.8µH	

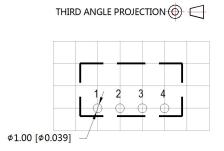
Note: In the case of actual use, the requirements for EMI are high, it is subject to ${\sf CY}$.

Dimensions and Recommended Layout



Unit :mm[inch]

Pin section tolerances :±0.10[±0.004] General tolerances:±0.25[±0.010]



Note: Grid 2.54*2.54mm

Pin-Out				
Pin	Function			
1	GND			
2	Vin			
3	0V			
4	+Vo			

Notes:

- 1. If the product is not operated within the required load range, the product performance cannot be guaranteed to comply with all parameters in the datasheet;
- 2. The maximum capacitive load offered were tested at input voltage range and full load;
- 3. Unless otherwise specified, parameters in this datasheet were measured under the conditions of Ta=25°C, humidity<75%RH with nominal input voltage and rated output load;
- 4. All index testing methods in this datasheet are based on our Company's corporate standards;
- 5. We can provide product customization service, please contact our technicians directly for specific information;
- 6. Products are related to laws and regulations: see "Features" and "EMC";
- Our products shall be classified according to ISO14001 and related environmental laws and regulations, and shall be handled by qualified units.

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